

In the Claims:

1. (Currently Amended) A system, comprising:

a signal generator coupled to an input of a signal line, the signal generator generating a signal of a particular frequency;

at least one receiving device electrically coupled to an output of the signal line, wherein the at least one receiving device comprises a clock generator, wherein the clock generator is synchronized to the signal and generates a clock signal, wherein the clock signal comprises a frequency less than a frequency of the signal of a particular frequency;
and

an impedance element coupled to the input of the signal line, the impedance element comprising an impedance chosen to create a resonant condition at the input of the signal line, wherein the resonant condition comprises a resonant frequency that essentially coincides with the frequency of the signal.

~~a synchronizer signal generator device, to be connected to an electronic system, the synchronizer signal generator device to emit a synchronizer signal of a particular frequency, the synchronizer signal to be transferred to at least one receiving device of the electronic system;~~

~~at least one additional device, for which an impedance is chosen such that a resonance oscillatory circuit is created for the synchronizer signal generator device having a resonance frequency which essentially coincides with the frequency of the synchronizer signal; and~~

~~a clock generator device to generate a clock signal having a frequency, wherein~~

~~the clock generator device is controlled by the synchronizer signal, and~~

~~the frequency of the synchronizer signal is greater than the frequency of the clock signal.~~

2. (Currently Amended) The system according to Claim 1, wherein the signal synchronizer ~~signal transferred to the at least one receiving device~~ is essentially sinusoid.

3. (Currently Amended) The system according to Claim 1, wherein the signal generator comprises a driver device, ~~further comprising a driver device to generate the synchronizer signal.~~

4. (Currently Amended) The system according to Claim 1, wherein the signal generator driver device generates an essentially rectangular signal.

5. (Currently Amended) The system according to Claim 4, wherein the essentially rectangular signal generated by the driver device is ~~filtered, wherein a signal present at an input node of the signal line such that the signal emitted by the synchronizer signal generator device~~ is essentially sinusoid.

6. (Currently Amended) The system according to Claim 1, wherein the impedance element comprises ~~further comprising at least one impedance device, which has an inductive component.~~

7. (Currently Amended) The system according to claim 6, wherein the impedance element comprises ~~at least one impedance device~~ has a capacitive component.

8. (Currently Amended) The system according to Claim 7, wherein an inductance of the inductive component and/or the capacitance of the capacitive component ~~an inductivity and/or capacitance adjustment of the inductive and/or capacitive component~~ is set during a manufacture of the system.

9. (Currently Amended) A system according to Claim 8, wherein the inductance and/or capacitance ~~inductivity and/or the capacitance of the inductive and/or capacitive component~~ is variably adjustable after the manufacture of the system.

10. (Currently Amended) The system according to Claim 9, wherein the capacitive component ~~[[is]]~~ comprises a capacitive diode.

11. (Currently Amended) The system according to Claim 1, wherein the at least one receiving device comprises ~~to which the synchronizer signal is to be transferred,~~ is a semi-conductor component.

12. (Currently Amended) The system according to Claim 1, wherein ~~[[the]]~~ at least one receiving device uses the synchronizer signal ~~is to be used~~ for chronological co-ordination of relaying and/or processing and/or transfer of data.

13. (Currently Amended) The system according to Claim 1, wherein at least one receiving device ~~the synchronizer signal generator~~ device generates a further signal under control of the ~~synchronizer~~ signal, wherein the further signal ~~which is to be used~~ for chronological co-ordination of relaying and/or processing and/or transfer of data.

14. (Currently Amended) The system according to Claim 13, wherein the further signal ~~[[has]]~~ comprises a lower frequency than the ~~synch~~ronizer signal.

15. (Currently Amended) The system according to Claim 14, wherein the at least one receiving device comprises a PLL or DLL circuit, wherein the PLL or DLL circuit generates ~~is used to generate~~ the further signal.

16. (Currently Amended) A process for generating a synchronizer, the process comprising:

transmitting ~~emitting~~ a synchronizer signal from a ~~synch~~ronizer signal generator device to at least one receiving device in~~[[of]]~~ an electronic system;

coupling ~~providing~~ at least one additional device at an output of the signal generator, the at one additional device comprising an impedance for which an impedance ~~has been selected~~ such that~~[[,]]~~ a resonant~~[[resonance]]~~-oscillatory condition ~~circuit~~ is created at an output of the signal generator which essentially coincides with a frequency ~~of the signal;~~

adjusting ~~[[the]]~~ a center frequency of the resonant-oscillatory condition, wherein the center frequency modified to essentially coincide with a frequency of the signal;

generating a clock signal synchronized to the signal, wherein a frequency of the clock signal is less than the frequency of the signal, wherein the clock signal is generated ~~by at least one receiving device, at a clock generator device, wherein~~

~~the clock generator device is controlled by the synchronizer signal, and~~

~~the frequency of the synchronizer signal is greater than a frequency of the clock signal.~~

17. (New) The process of claim 16, wherein the adjusting the center frequency comprises switching on or off at least one additional device coupled to the output of the signal generator.

18. (New) The process of claim 17, wherein the at least one additional device comprises a capacitive diode.

19. (New) The process of claim 17, wherein that at least one additional device comprises at least two additional devices, the at least two additional devices being connected in parallel.